

IN THE CLAIMS

Claim 1 (Currently Amended): A method comprising:

automatically transferring time sensitive data from a first storage of a first processor-based system to a second storage of a second processor-based system when it is determined that the first processor-based system is being powered off, wherein the second processor-based system is located in a housing of the first processor-based system; and

automatically displaying said time sensitive data on a display of said second processor-based system at a predetermined time; and

powering said second processor-based system using a power source of said first processor-based system when said first processor-based system is powered off.

Claims 2-5 (Canceled).

Claim 6 (Original): The method of claim 1 including providing real time clock information from said first processor-based system to said second processor-based system.

Claim 7 (Canceled).

Claim 8 (Previously Presented): A machine-readable storage medium including instructions that when executed cause a second processor-based system to:

automatically receive time sensitive data from a first storage of a first processor-based system at a second storage of the second processor-based system; and

automatically display said time sensitive data on a display of said second processor-based system at a predetermined time.

Claim 9 (Previously Presented): The article of claim 8 further storing instructions that cause the first processor-based system to automatically transfer data from the storage coupled to the first processor-based system when it is determined that the first processor-based system is being powered off.

Claim 10 (Previously Presented): The article of claim 8 further storing instructions that cause the first processor-based system to automatically transfer personal information manager information.

Claim 11 (Previously Presented): The article of claim 10 further storing instructions that cause the first processor-based system to automatically transfer timed alerts.

Claim 12 (Previously Presented): The article of claim 8 further storing instructions that cause the second processor-based system to automatically provide an audible alert at [[a]] the predetermined time.

Claim 13 (Previously Presented): The article of claim 8 further storing instructions that cause the first processor-based system to provide real time clock information from said first processor-based system to said second processor-based system.

Claim 14 (Canceled).

Claim 15 (Currently Amended): A processor-based system comprising:
a processor;
a first storage to store a personal information manager application; and
a second storage to store software including instructions that cause the processor to automatically transfer time sensitive data to a second processor-based device for display of said time sensitive data at a predetermined time, said automatic transfer to occur in response to an indication that said processor-based system is to be powered off; and
a link on said system to said second processor-based device, wherein said processor-based system is a portable computer that includes said second processor-based device, wherein said second processor-based device is to be powered on when said processor-based system is to be powered off.

Claims 16 and 17 (Canceled).

Claim 18 (Currently Amended): The system of claim [(17)] 15 including a display for said second processor-based device and a housing for said portable computer, said display being located on the outside of said housing.

Claim 19 (Canceled).

Claim 20 (Previously Presented): The system of claim 15 wherein said processor is to automatically transfer said time sensitive data to said second processor-based device when the processor detects that the processor-based system will be turned off.

Claim 21 (Canceled).

Claim 22 (Previously Presented): The method of claim 1 wherein said second processor-based system comprises a standby system.

Claim 23 (Previously Presented): The method of claim 1 including automatically displaying said time sensitive data on said display of said second processor-based system located on an exterior of said housing.

Claim 24 (Previously Presented): The method of claim 1 including automatically displaying said time sensitive data on said display of said second processor-based system while said first processor-based system is powered off.

Claim 25 (Currently Amended): The system of claim [(17)] 15, wherein said second processor-based device comprises a standby system.

Claim 26 (Canceled).